UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,299,355 B2

Page 1 of 1

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INVENTOR(S)

: Zheng Qi

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 1, Column 12, Line 51, please delete "theoutput" and replace with -- the output--.

In Claim 2, Column 12, Line 56, please delete "thee 32-bit CSAs" and replace with --three 32-bit CSAs--.

In Claim 4, Column 12, Line 62, please delete "wherein combined adder tree includes add 5tol and add 4tol adders." and replace with --wherein the combined adder tree includes add5tol and add4tol adders .-- .

Column 3, Lines 56-58, please delete "As noted above, of the two algorithms supported by the IPSec protocol, HMAC-SHA1-96 is about twenty-five percent slower".

Signed and Sealed this

Twenty-ninth Day of April, 2008

JON W. DUDAS Director of the United States Patent and Trademark Office